

100

Given Range	Optimized Set
4788 - 5593	4788, 4789, 479X, 48XX, 49XX, 50XX, 51XX, 52XX, 53XX, 54XX, 550X, 551X 552X, 553X, 554X, 555X, 556X, 557X, 558X, 5590, 5591, 5592, 5593
800 - 2999	8XX, 9XX, 1XXX, 2XXX

Figure 1A

150

Given Range	Sub-Range 1	Sub-Range 2	Sub-Range 3	Sub-Range 4
4318-5689	4318-4319	4320-4999	5000-5679	5680-5689
560-2346	N/A	560-1999	2000-2339	2340-2346
4300-4399	N/A	N/A	4300-4389	4390-4399
4476-7688	4476-4479	4480-6999	7000-7679	7680-7688

Figure 1B

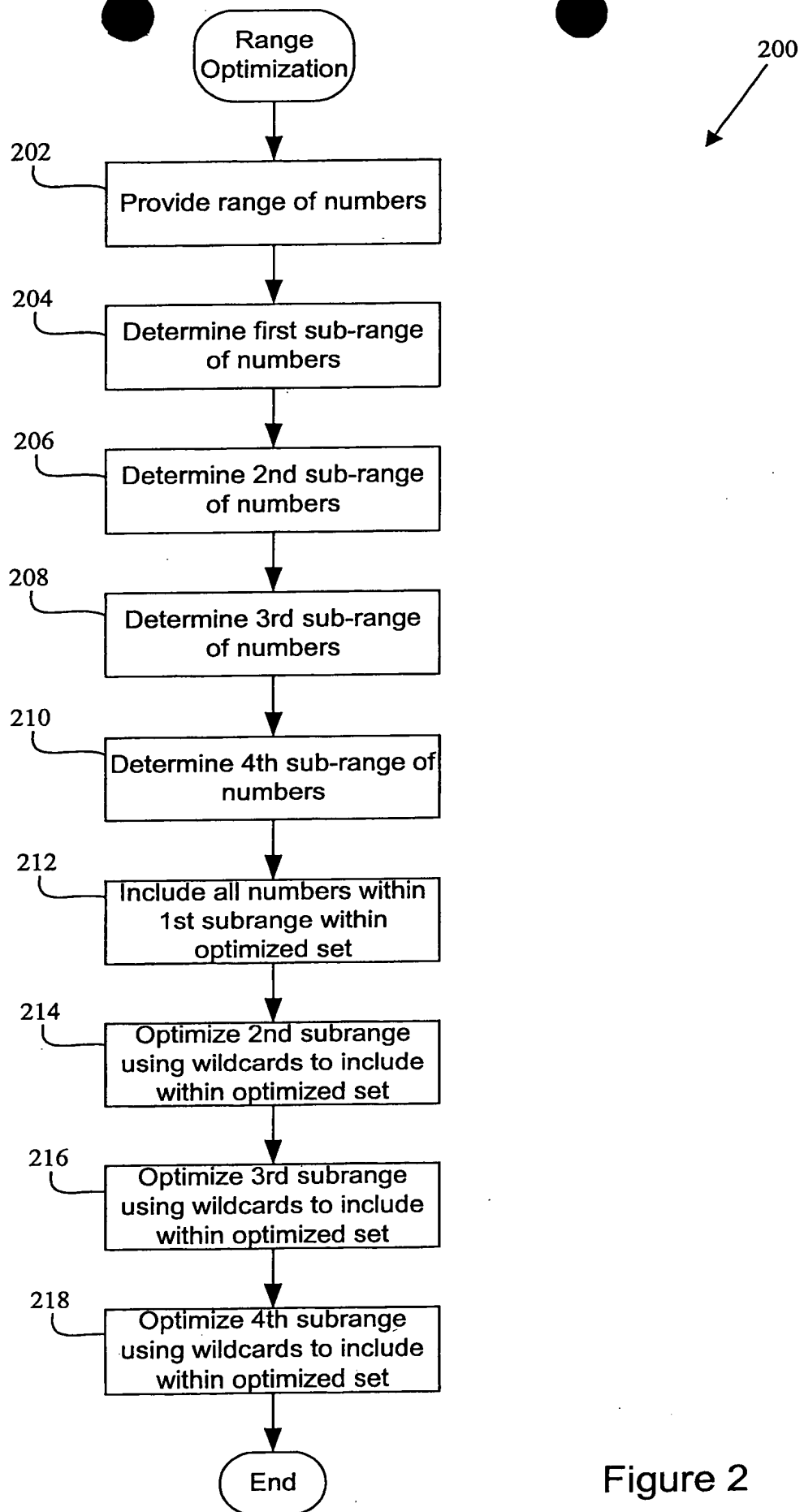


Figure 2

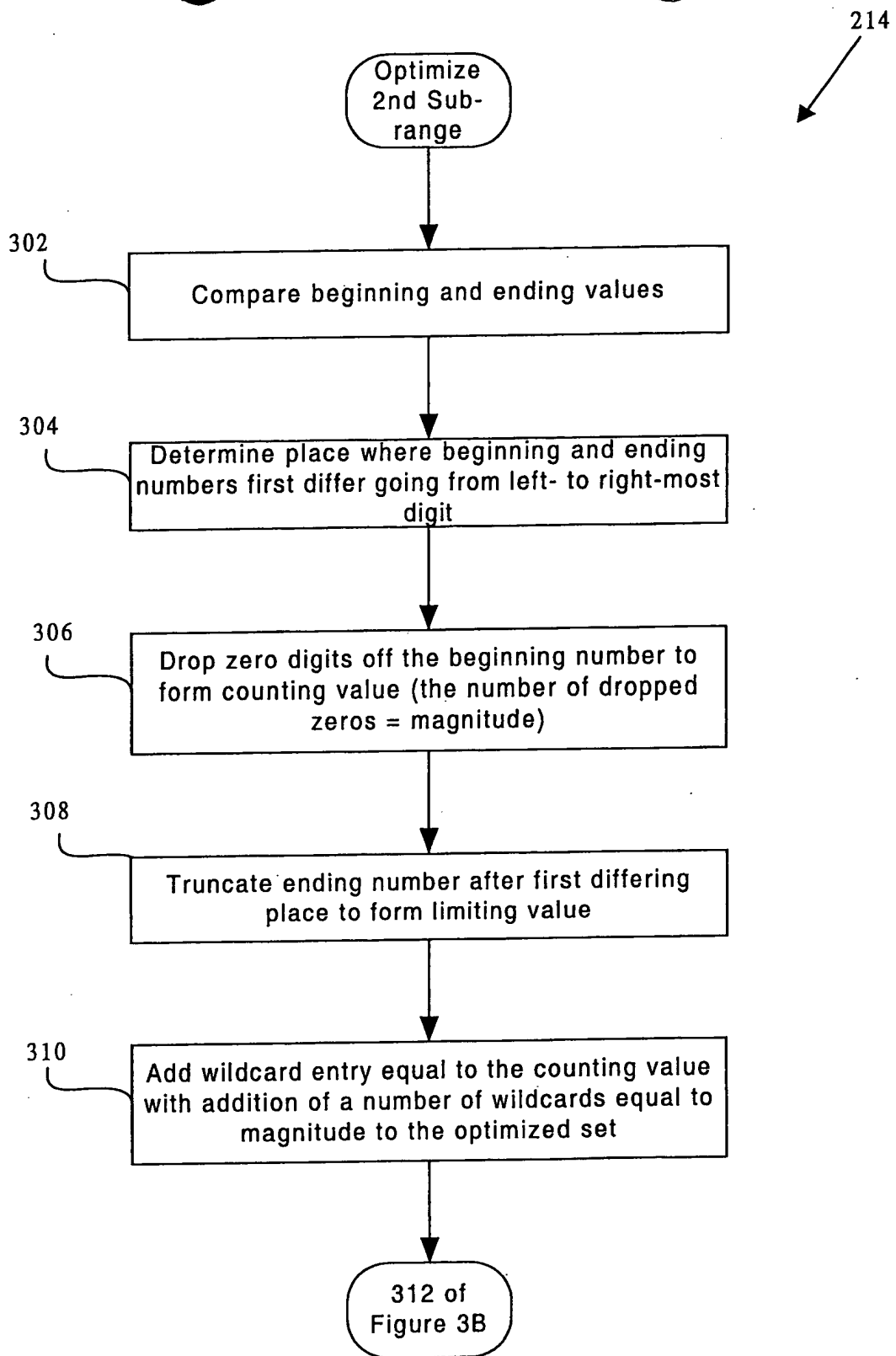


Figure 3A

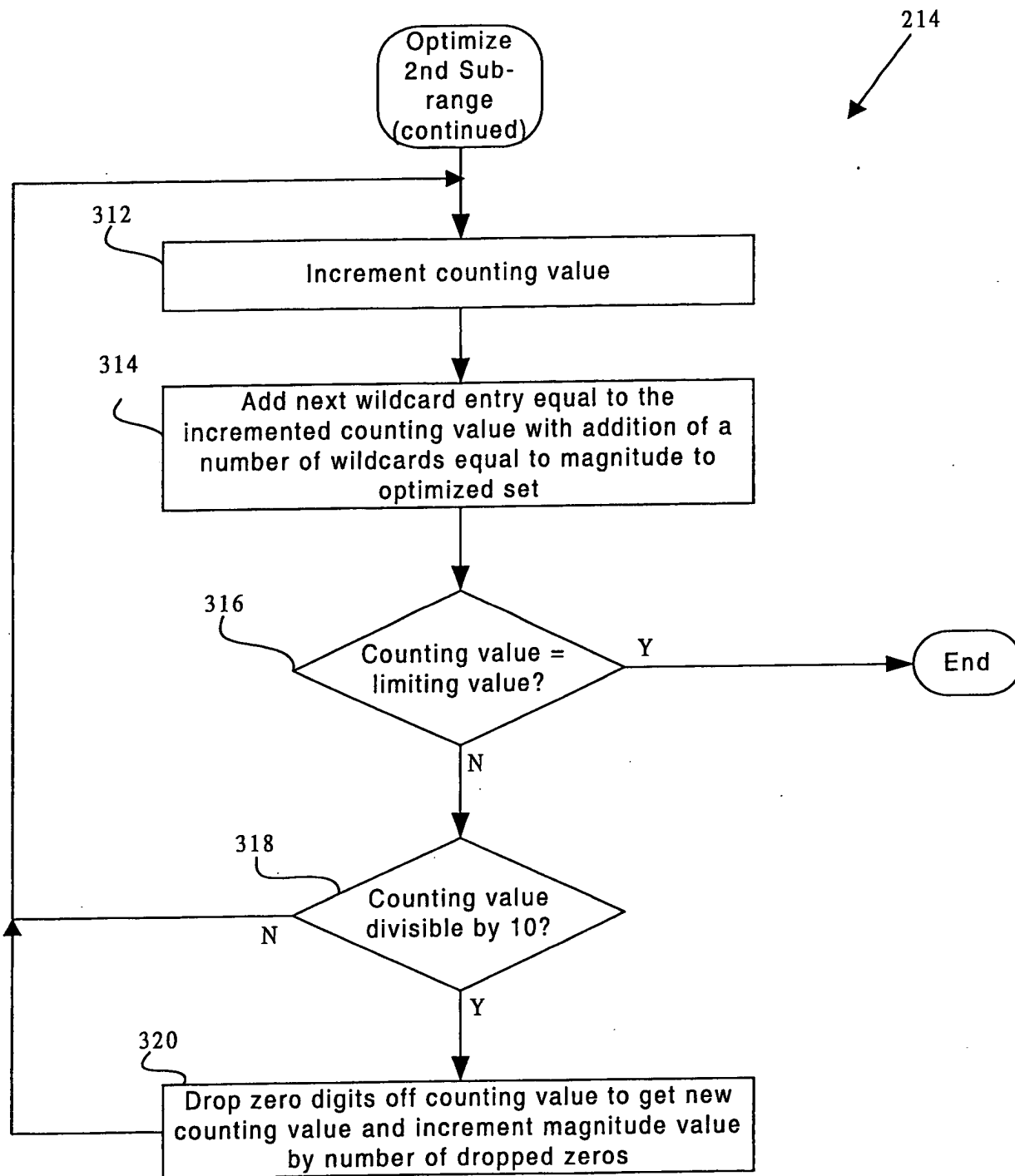


Figure 3B

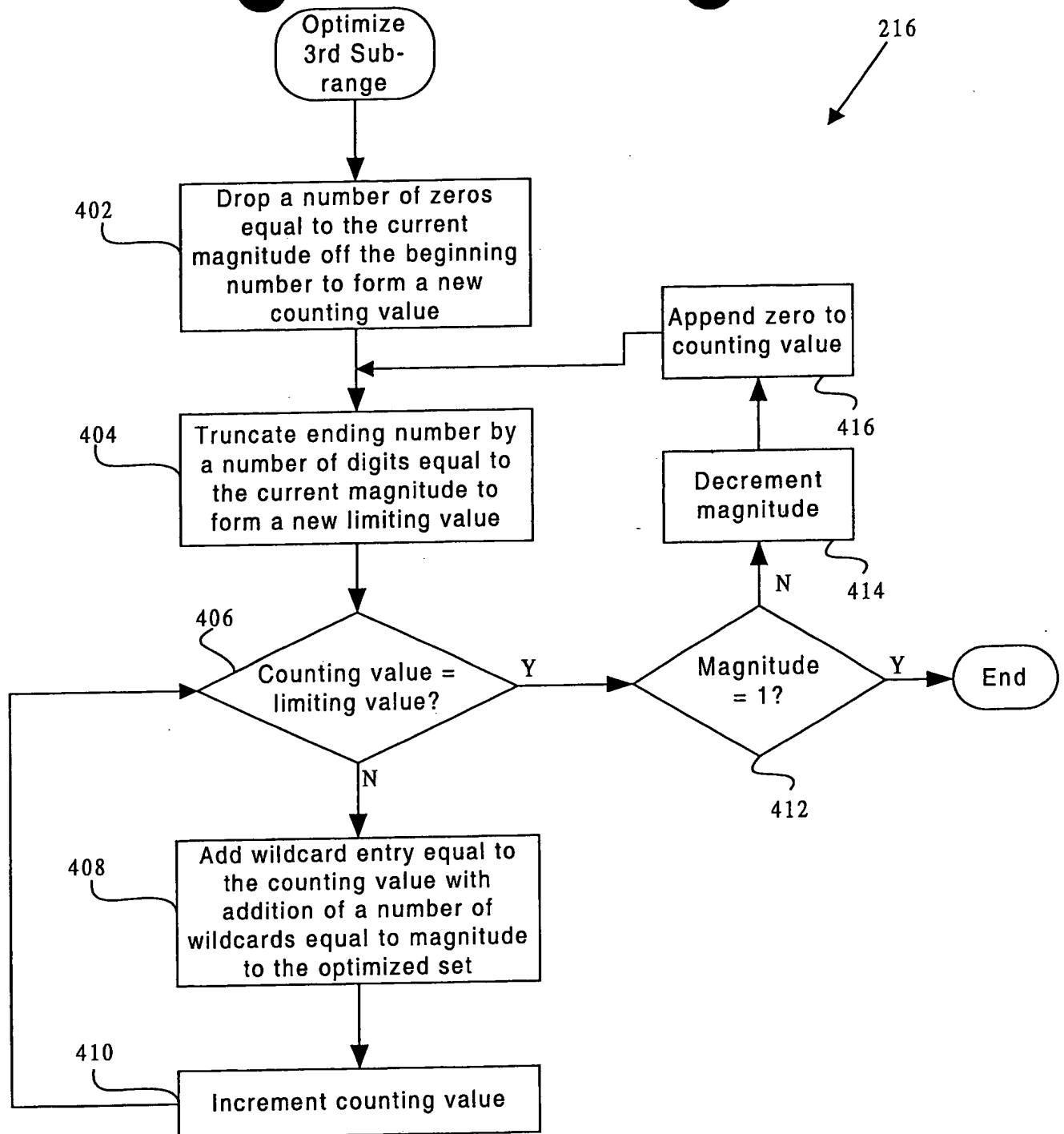


Figure 4

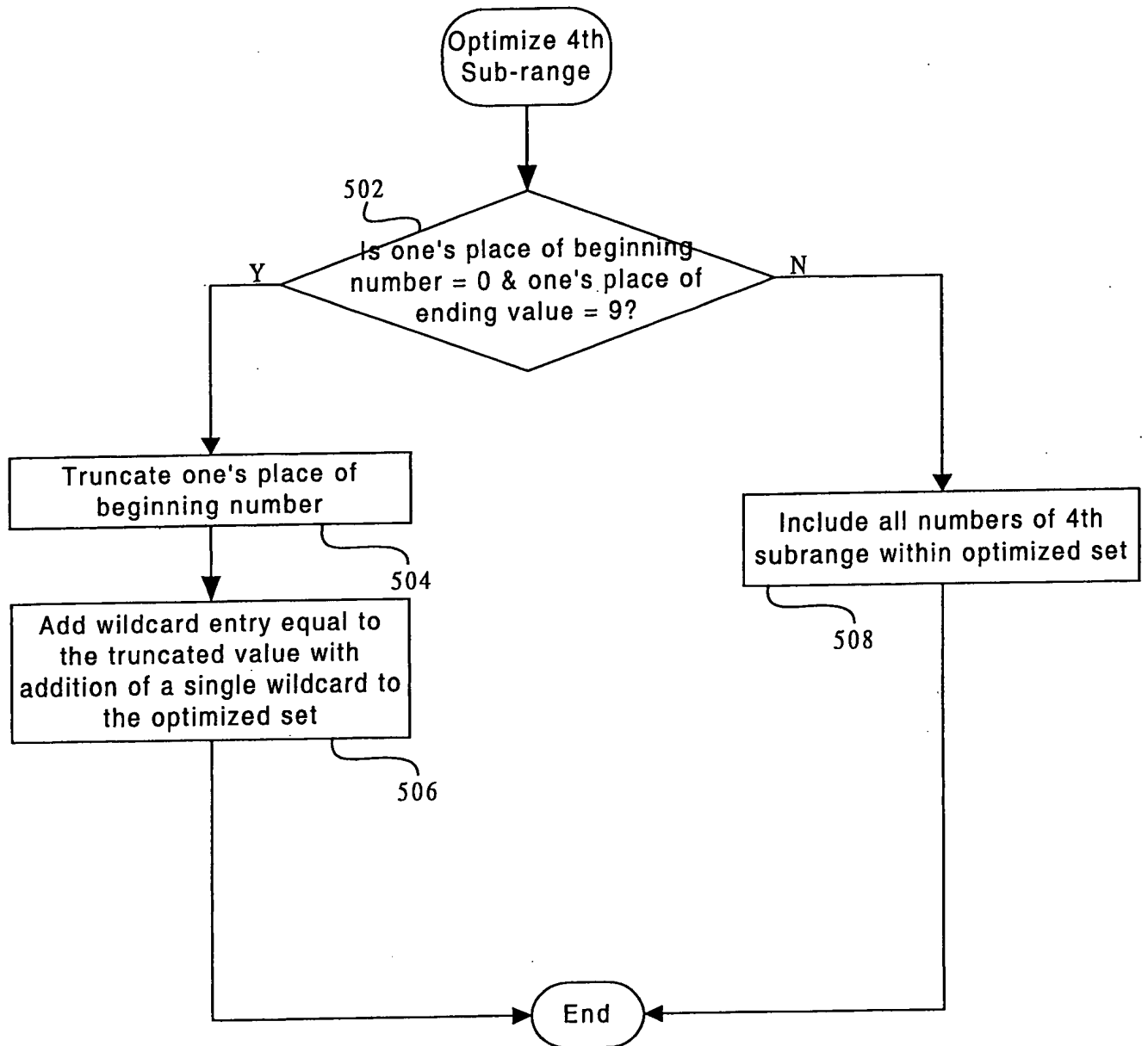


Figure 5

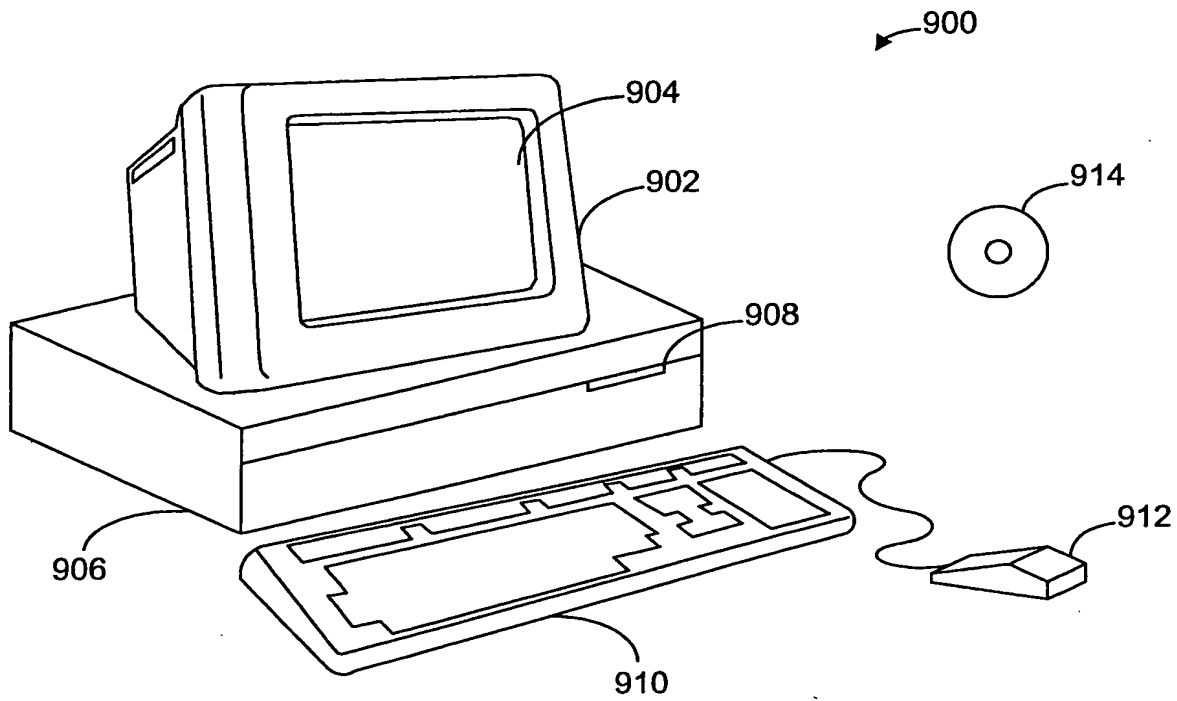


Figure 6A

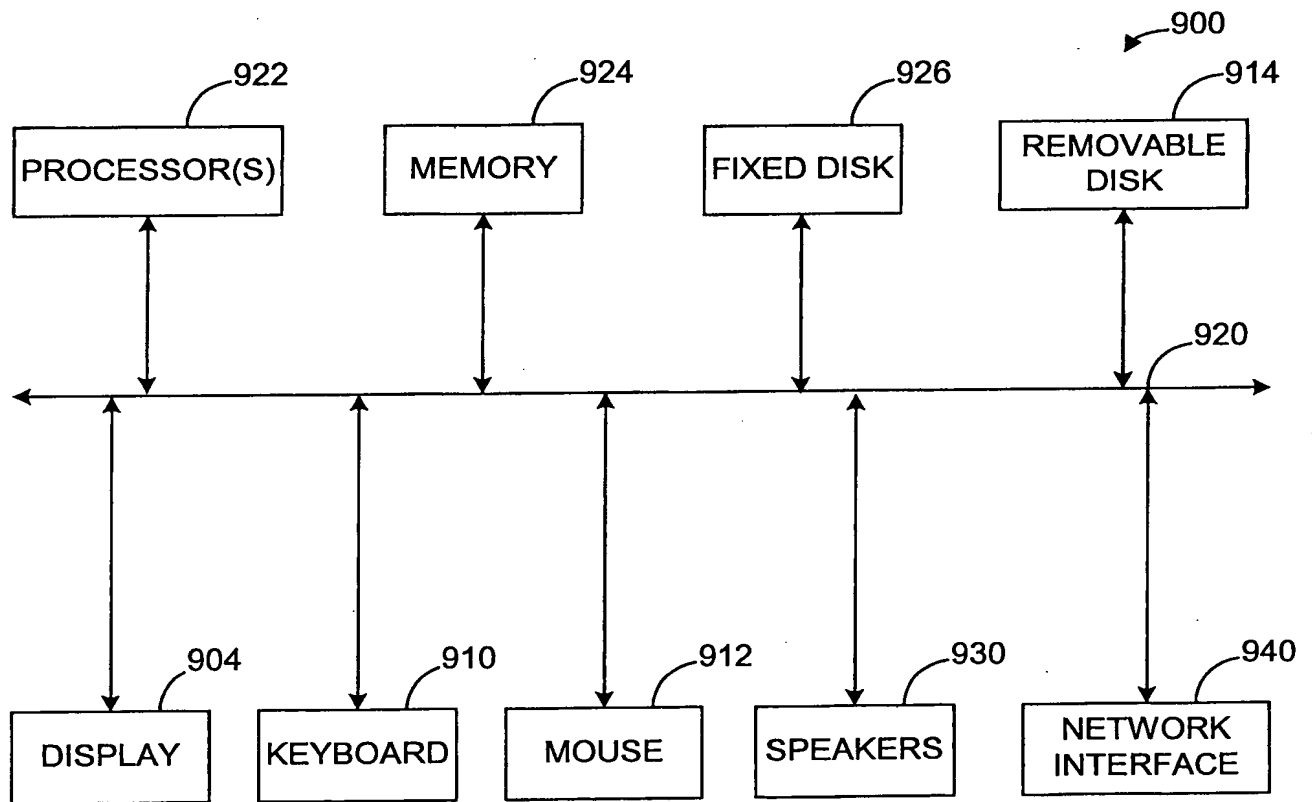


Figure 6B

FIG. 7 is a block diagram of a system 10, in accordance with one embodiment of the present invention. The system 10 includes a processor 63 and a memory 62, which are connected to a bus 15. The system 10 also includes an interface 68, which is connected to the bus 15. The interface 68 is connected to a network 100, which is connected to a server 102. The server 102 is connected to a database 104, which is connected to a user 106. The user 106 is connected to the system 10.

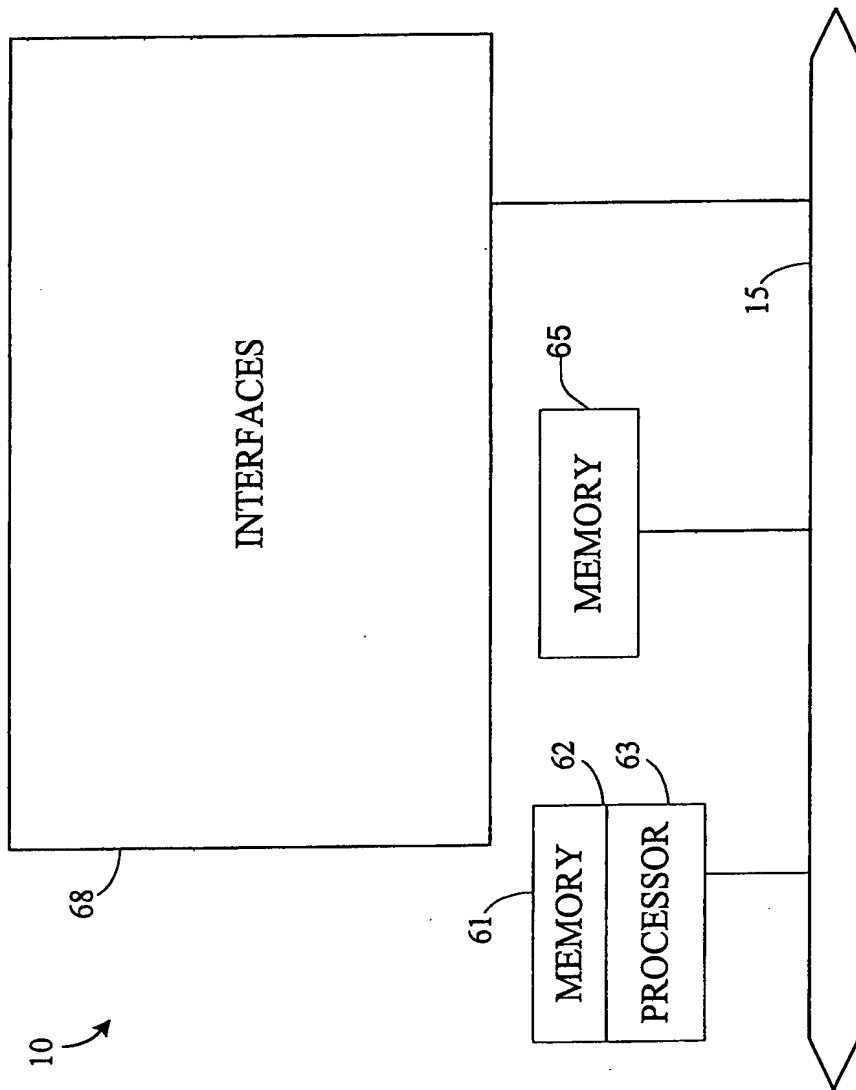


Figure 7